+8V POWER GOOD AND NOT BATTERY

POWER GOOD

INT

PULSES 0 IF A WRITE TO A PROTECTED BLOCK IS ATTEMPTED

MEM PROTECT STATUS
0=MEM PROTECTED

processor write

CLR INT (SOFTWARE)

CLR INT (HARDWARE)

WRITE PROTECTION VIOLATION INTERRUPT

=0 WHEN BOARD SELECTED DURING A MEMORY CYCLE AND THAT BLOCK IS WRITE PROTECTED.

2N3904

BOARD SELECT OCCURS ON CORRECT 4K ADDRESS BLOCK ON MEMORY R/W AND ON UPPER NIBBLE OF CPU OUTPUT TO I/O @FE

DIP SWITCH

=0 ON MEMORY R/W ACCESS

=0 CPU OUTPUT

=1 CPU INPUT

FRONT PANEL CONTROL

SOFTWARE CONTROL

MEMORY WRITE PROTECT LATCHES 7474

RED LED'S

=1 WHEN BANK IS SELECTED AND NOT WRITE PROTECTED

=0 ON MEM WRITE

=0 WHEN CHIP SELECTED AND NOT WRITE PROTECTED AND POWER GOOD

RAM/BANK CHIP ENABLE

RAM/BANK READ / WRITE

GREEN LED'S

ENABLE STATUS LATCH

ENABLE STATUS READ

4K RAM BLOCK	DIP SWITCH 8	7	6	5	4	3	2	1
0xxx	0	1	0	1	0	1	0	1
1xxx	0	1	0	1	0	1	1	0
2xxx	0	1	0	1	1	0	0	1
3xxx	0	1	0	1	1	0	1	0
4xxx	0	1	1	0	0	1	0	1
5xxx	0	1	1	0	0	1	1	0
6xxx	0	1	1	0	1	0	0	1
7xxx	0	1	1	0	1	0	1	0

4K RAM BLOCK	DIP SWITCH 8	7	6	5	4	3	2	1
8xxx	1	0	0	1	0	1	0	1
9xxx	1	0	0	1	0	1	1	0
Axxx	1	0	0	1	1	0	0	1
Bxxx	1	0	0	1	1	0	1	0
Cxxx	1	0	1	0	0	1	0	1
Dxxx	1	0	1	0	0	1	1	0
Exxx	1	0	1	0	1	0	0	1
Fxxx	1	0	1	0	1	0	1	0

CPU OUTPUT TO PORT FE
7 6 5 4 3 2 1 0
BOARD SELECT
1 OF 4 1K BLOCK SELECT
COMMAND
0 0 - CLEAR INT
0 1 - UNPROTECT BLOCK
1 0 - PROTECT BLOCK
1 1 - SELECT BOARD FOR STATUS READ

CPU INPUT PORT FE
7 6 5 4 3 2 1 0
BOARD * SELECTED
WRITE PROTECT STATUS

*THE BOARD IS SELECTED WHEN EVER THERE HAS BEEN AN WRITE PROTECT VIOLATION OR BOARD HAS BEEN SELECTED THROUGH CPU OUTPUT

READ BOARD STATUS ON CPU INPUT OF I/O @FE.
(STATUS OR INT) AND sINP AND pDBIN AND MEM=FE

READ STATUS

READ MEMORY

IMSAI 8080

RAM 4A

JOSH BENSADON

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